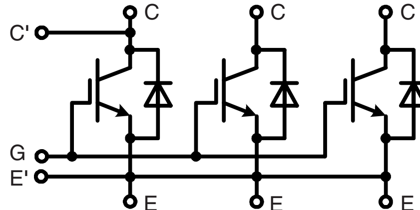
IGBT Module
Single switch

I_{C80} = 1800 A
V_{CES} = 1700 V
$V_{CE(sat)\ typ.}$ = 2.3 V

Short Circuit SOA Capability
Square RBSOA



IGBT

Symbol	Conditions	Maximum Ratings	
V_{CES}	V_{GE} = 0 V	1700	V
V_{GES}		± 20	V
I_{C80}	T_C = 80°C	1800	A
I_{CM}	t_p = 1 ms; T_C = 80°C	3600	A
t_{SC}	V_{CC} = 1000 V; $V_{CEM\ CHIP}$ = ≤1700 V; V_{GE} ≤ 15 V; T_{VJ} ≤ 125°C	10	µs

Symbol	Conditions	Characteristic Values (T_{VJ} = 25°C, unless otherwise specified) min.	typ.	max.	
$V_{CE(sat)}$ ①	I_C = 1800 A; V_{GE} = 15 V; T_{VJ} = 25°C T_{VJ} = 125°C		2.3 2.6	2.6 2.9	V V
$V_{GE(th)}$	I_C = 240 mA; V_{CE} = V_{GE}	4.5		6.5	V
I_{CES}	V_{CE} = 1700 V; V_{GE} = 0 V; T_{VJ} = 125°C			120	mA
I_{GES}	V_{CE} = 0 V; V_{GE} = ± 20 V; T_{VJ} = 125°C			500	nA
$t_{d(on)}$ t_r $t_{d(off)}$ t_f E_{on} E_{off}	Inductive load; T_{VJ} = 125°C; V_{GE} = ±15 V; V_{CC} = 900 V; I_C = 1800 A; R_G = 0.82 Ω; L_σ = 60 nH		285 230 950 240 530 670		ns ns ns ns mJ mJ
C_{ies} C_{oes} C_{res}	V_{CE} = 25 V; V_{GE} = 0 V; f = 1 MHz		166 16.5 7.0		nF nF nF
Q_{ge}	I_C = 1800 A; V_{CE} = 900 V; V_{GE} = ± 15 V		15.1		µC
R_{thJC}				0.009	K/W

① Collector emitter saturation voltage is given at chip level

Features

- NPT³ IGBT
  - Low-loss
  - Smooth switching waveforms for good EMC
- Industry standard package
  - High power density
  - AlSiC base-plate for high power cycling capacity
  - AlN substrate for low thermal resistance

Typical Applications

- AC power converters for
  - industrial drives
  - windmills
  - traction
- LASER pulse generator

IXYS reserves the right to change limits, test conditions and dimensions.

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Diode

Symbol	Conditions	Maximum Ratings	
I_{F80}	T_C = 80°C	1800	A
I_{FSM}	V_R = 0 V; T_{VJ} = 125°C; t_p = 10 ms; half-sinewave	16500	A

Symbol	Conditions	Characteristic Values min.	typ.	max.	
V_F ②	I_F = 1800 A; T_{VJ} = 25°C		1.65	2.0	V
	T_{VJ} = 125°C		1.70		V
I_{RM}	V_{CC} = 900 V; I_C = 1800 A; V_{GE} = ±15 V; R_G = 1.5 Ω; T_{VJ} = 125°C; Inductive load; L_σ = 60nH		1470		A
t_{rr}			870		ns
Q_{RR}			770		µC
E_{rec}			530		mJ
R_{thJC}				0.017	K/W

② Forward voltage is given at chip level

Module

Symbol	Conditions		Maximum Ratings	
T_{JM}	max. junction temperature		+150	°C
T_{VJ}	Operating temperature		-40...+125	°C
T_{stg}	Storage temperature		-40...+125	°C
V_{ISOL}	50 Hz		4000	V~
M_d	Mounting torque	Base-heatsink, M6 screws	4 - 6	Nm
		Main terminals, M8 screws	8 - 10	Nm

Symbol	Conditions		Characteristic Values min.	typ.	max.	
d_A	Clearance distance	terminal to base	23			mm
		terminal to terminal	19			mm
d_S	Surface creepage distance	terminal to base	33			mm
		terminal to terminal	33			mm
L_σ	Module stray inductance, C to E terminal			10		nH
$R_{term-chip}$ *)	Resistance terminal to chip			0.085		mΩ
R_{thCH}	per module; λ grease = 1 W/m•K			0.006		K/W
Weight				1500		g

*) $V = V_{CE(sat)} + R_{term-chip} \cdot I_C$ resp. $V = V_F + R_{term-chip} \cdot I_F$

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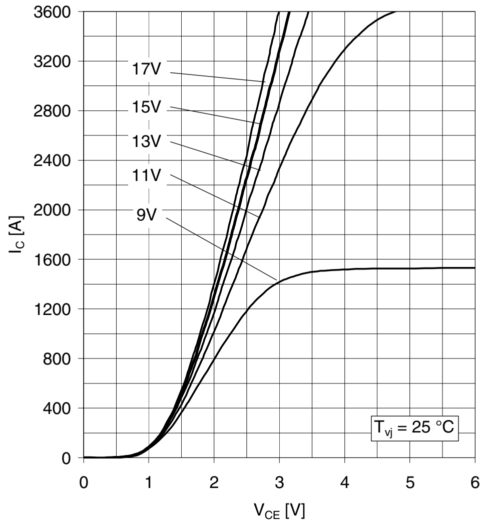

Fig. 1 Typical output characteristics, chip level

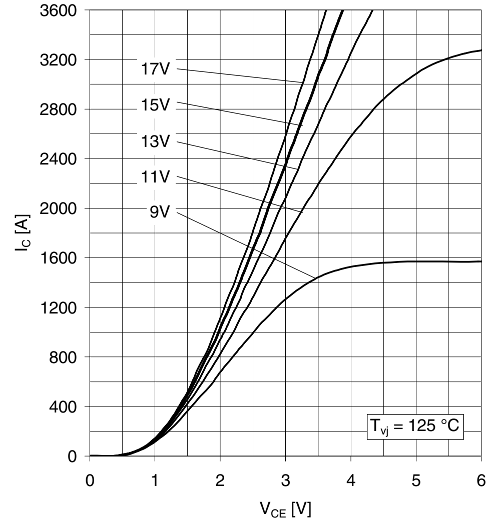

Fig. 2 Typical output characteristics, chip level

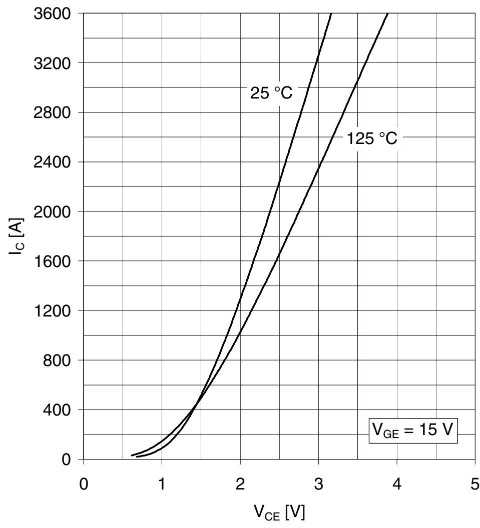

Fig. 3 Typical onstate characteristics, chip level

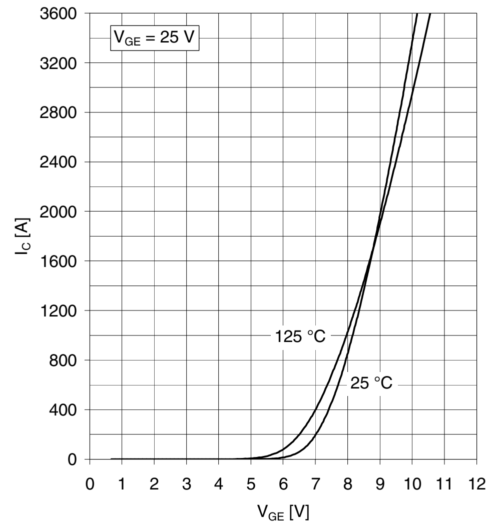

Fig. 4 Typical transfer characteristics, chip level

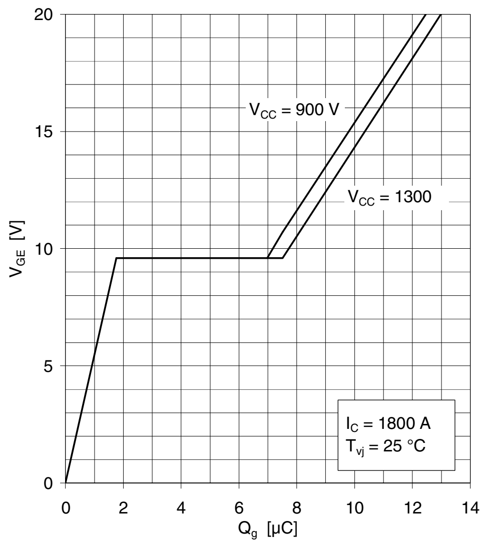

Fig. 5 Typical gate charge characteristics

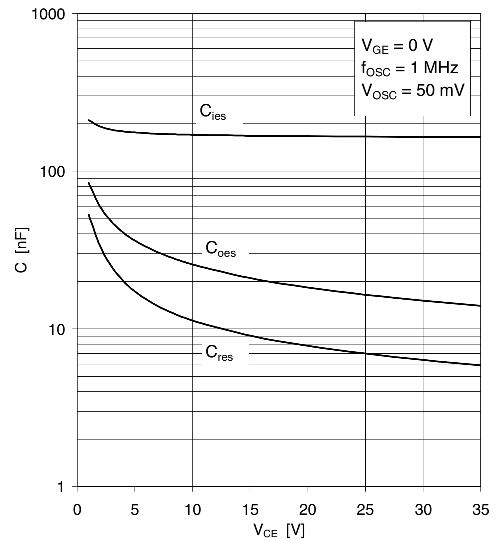

Fig. 6 Typical capacitances vs collector-emitter voltage

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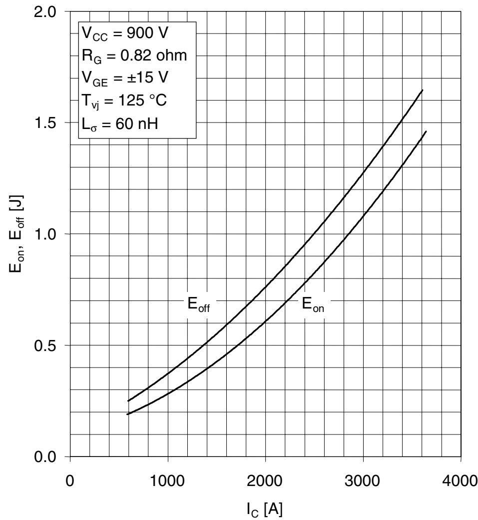

Fig. 7 Typical switching energies per pulse vs collector current

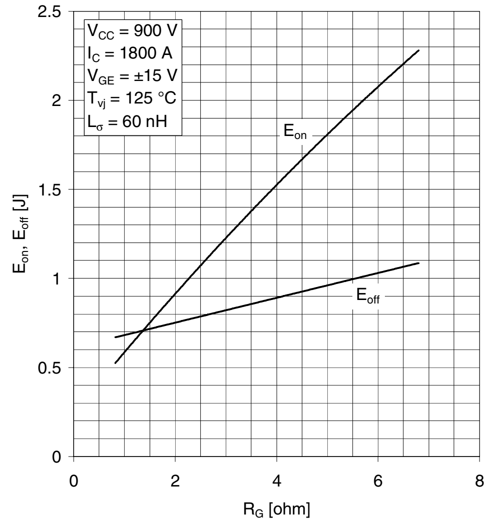

Fig. 8 Typical switching energies per pulse vs gate resistor

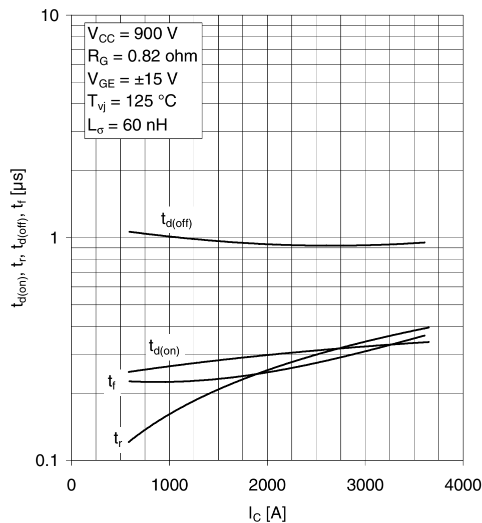

Fig. 9 Typical switching timesvs collector current

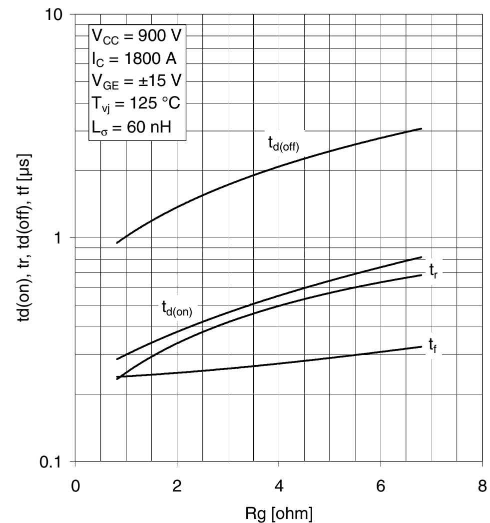

Fig. 10 Typical switching timesvs gate resistor

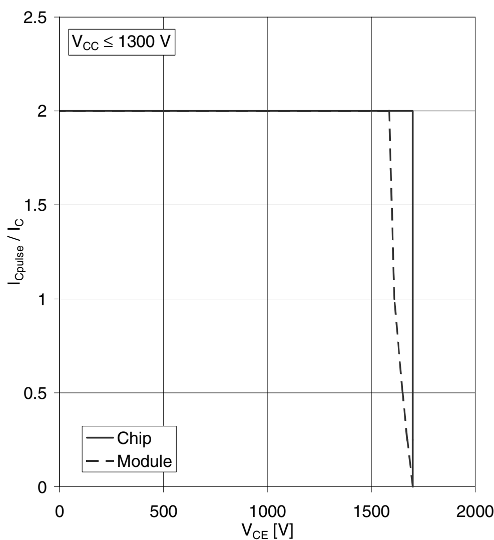

Fig. 11 Turn-off safe operating area (RBSOA)

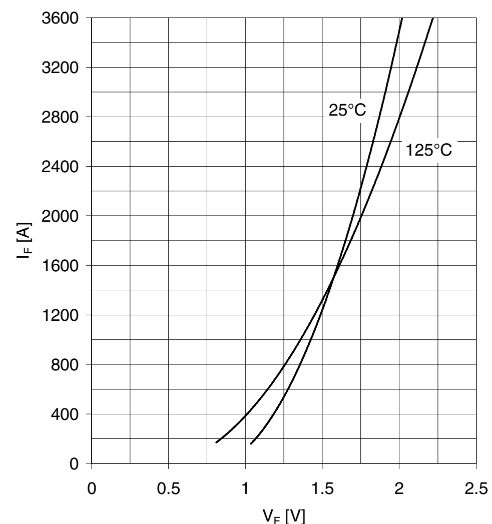

Fig. 12 Typical diode forward characteristics, chip level

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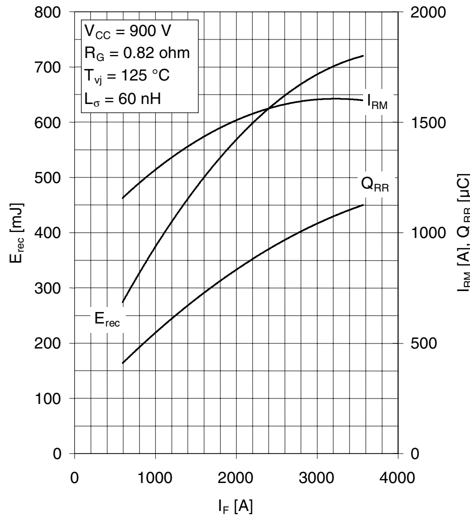

Fig. 13 Typical reverse recovery characteristics vs forward current

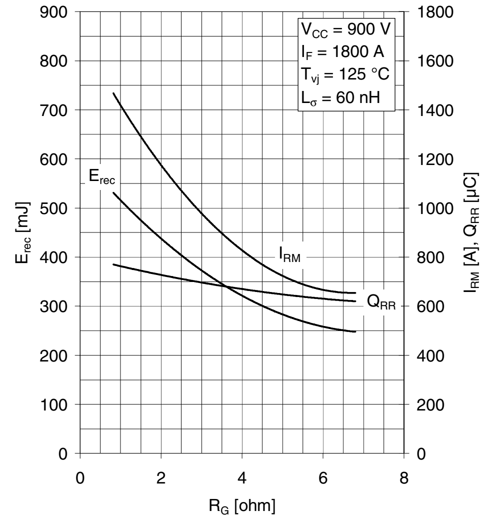

Fig. 14 Typical reverse recovery characteristics vs gate resistor

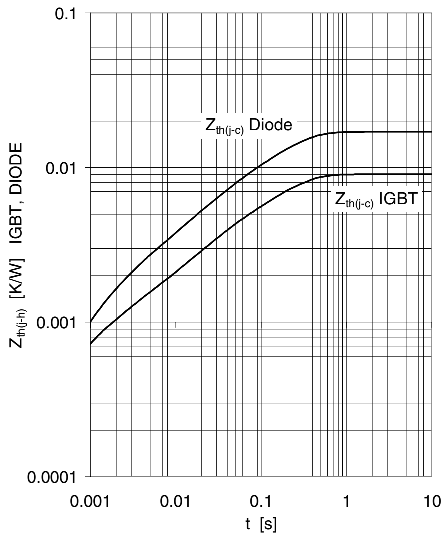

Fig. 15 Thermal impedance vs time

$$Z_{th\,(j\text{-}c)}(t) = \sum_{i=1}^{n} R_i\,(1 - e^{-t/\tau_i})$$

	i	1	2	3	4
IGBT	R_i(K/kW)	5.97	1.99	0.619	0.465
	τ_i(ms)	179	22	2.4	0.54
DIODE	R_i(K/kW)	11.1	3.36	1.27	1.34
	τ_i(ms)	189	30	7.4	1.4

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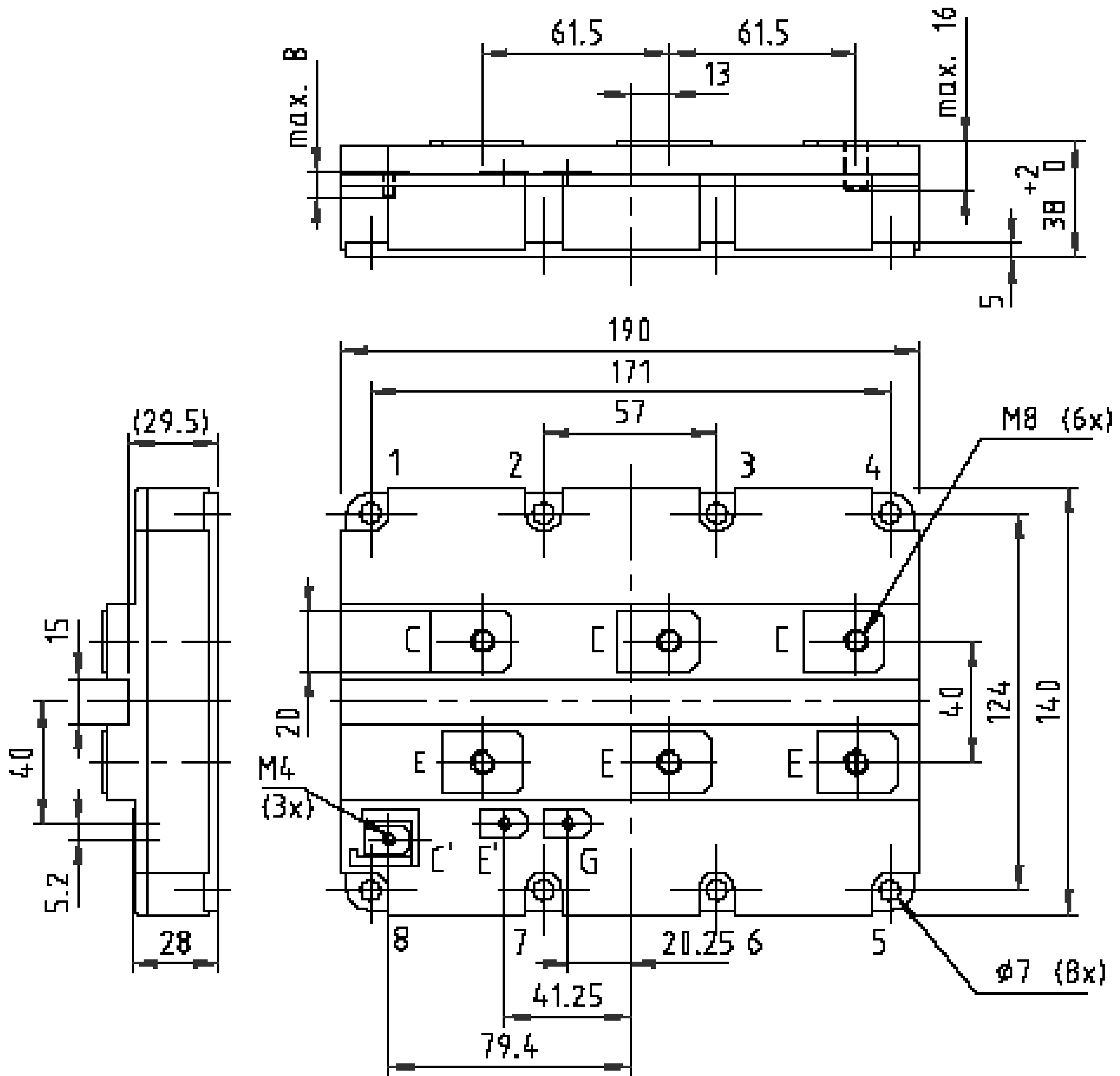
Outline drawing



Note: all dimensions are shown in mm

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